

Applicant	James D. Beasom	<b><u>SUBMISSION OF FORMAL DRAWINGS UNDER 37 C.F.R. §1.85</u></b>
Serial No.	09/992,880	
Filing Date	November 5, 2001	
Group Art Unit	2814	
Examiner Name	Hoai V. Pham	
Confirmation No.	7668	
Attorney Docket No.	125.014US01	
Title: INTEGRATED CIRCUIT WITH MOS CAPACITOR		

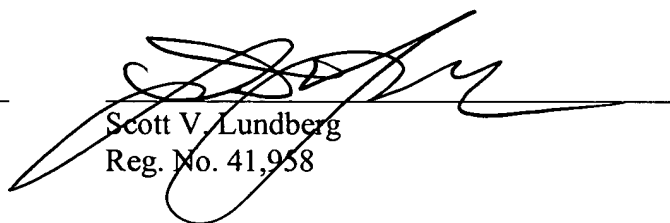
Official Draftsman c/o Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attached please find nine (9) sheets of Formal Drawings for the above-identified patent application.

Please contact the undersigned attorney if you have any questions.

Respectfully submitted,

Date: 9-17-01

  
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